



PRESS RELEASE

GATEline Adds FPGA I/O Synthesis Tool to Its PCB Offerings *Unique Taray Technology Synthesizes Optimum FPGA I/Os*

Tullinge (June 2, 2008) – GATEline has signed a distributor agreement with Taray, Inc., for distribution in Sweden, Norway, Finland and Denmark of 7Circuits®, an FPGA I/O synthesis tool from Taray®.

"This partnership provides a long-needed FPGA I/O planning solution in the design flow we are offering our customers" said Hans Lundberg of GATEline. "With this agreement, we can now offer our customers an integrated flow that will dramatically shorten their FPGA and PCB design cycle, and significantly improve their time to market."

"We are very excited to leverage GATEline's history of marketing expertise to bring 7Circuits to design teams," said Nagesh Gupta, chief executive officer of Taray. "7Circuits uses our patented I/O synthesis technology to radically improve the productivity of design teams that develop PC boards using large FPGAs. We are finding that our customers are saving up to 2 man-months on typical FPGA design projects, with even more savings for more complex designs."

"FPGA I/O pin assignment is a big problem for the large, complex FPGA designs seen today," said Hemant Shah, Marketing Director for PCB Products at Cadence Design Systems, Inc. "Traditional approaches require users to do this assignment manually. 7Circuits has a unique pin assignment synthesis approach that makes it easy to design-in large, complex FPGAs on PCBs. To reduce development costs associated with these systems, Cadence continues to work closely with Taray in delivering a solution that is tightly integrated with Cadence's PCB design technology."

Design teams often struggle with FPGA pin assignments, employing manually intensive methods that neglect the PCB layout until late in the design cycle. This results in increased PCB routing efforts with more layers and vias, both of which can impact manufacturing cost and signal integrity. In addition, users manually create the symbols and schematics that connect the FPGAs to the rest of the system. To insure consistency, data exchanged throughout the entire design process must be carefully managed for every change to every pin assignment on every FPGA.

7Circuits generates FPGA pin assignments that are optimized for both the FPGA and the PCB. Using its built-in FPGA library and applying a set of user-supplied constraints, 7Circuits' rule-based synthesis engine automatically creates the FPGA pin connections as well as the symbols and schematics for use by the system and PCB designers. Furthermore, it maintains this data throughout the design cycle. It also minimizes or eliminates the possibility for user-induced errors.

As a result, customers can decrease their design cycles by as much as 5-15x, depending on the number of FPGAs in the design.

About Taray, Inc.

Taray, Inc., founded in 2002, provides cutting-edge tools for FPGA and PCB design and owns key patents in this area. Their flagship product, 7Circuits, is an FPGA I/O synthesis tool that incorporates Taray's patented technology to solve the FPGA/PCB co-design challenge.

Drawing on over 60 years of combined industry experience in the FPGA and EDA markets, Taray's executive management team has guided the company through six years of sustained profitability. For more information please visit www.tarayinc.com."

About GATEline AB

GATEline has since 1984 been established as the leading distributor on the Nordic market of software for electronic design (EDA). A very important part of our offerings are our services like technical support, training, design libraries and more. We are a Channel Partner to Cadence Design Systems and also representing well-known companies like Valor and Omnify Software. More information www.gateline.se

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